

FLOTS DE CONCEPTION COMPLETS DISPONIBLES A LA DATE DU 10/01/2008

Fonctionnalités	Cadence DFW II 5.0_33 (I:31F) (M)	Cadence DFW II 5.1_41 (I:31F) (M)	Cadence DFW II 6.1 (I:31F) (M)	Mentor Graphics VC4 (I:30F) (M)	Mentor Graphics V2005 (I:30F) (M)	Mentor Graphics V2007 (I:30F) (M)	Synopsys 2006_06 (I:20F) (M)	Synopsys 2007 (I:20F) (M)	Xilinx Alliance 2.1i (M1 4.12) (M)
Simulateur VHDL	NC Sim	NC Sim	NC Sim	QuickHDL ModelSim V5.7e	QuickSimPro ModelSim V6.0a	QuickSimPro ModelSim v6.2e	Sirocco Cyclone VHDL	VCS	Foundation Express (Synopsys) (1:PC)
Simulateur VHDL AMS	AMS Designer	AMS Designer	AMS Designer	Advance MS v1.5.1	Advance MS v2005.3	Advance MS v2007	Saber	Saber	----
Synthèse RTL	Build Gates PKS	RTL Compiler Ambit	RTL Compiler	Autologic II Leonardo Spectrum	Precision Synthesis Leonardo Spectrum	Precision Synthesis Leonardo Spectrum	Design Vision FPGA Express II Synopsys Graphical	Design Vision Design Compiler FPGA Synopsys Graphical	Foundation Express (Synopsys) (1:PC)
Saisie de Schema	Design Composer	Design Composer	Design Composer	Design Architect	Design Architect	Design Architect	Environnement	Environnement	dsngmgr
Simulateur Logique	Verilog XL NC Sim	Verilog XL NC Sim	Verilog XL NC Sim	QuickSim II	QuickSim II	QuickSim II	----	----	Foundation Functional Simulator (1:PC)
Analyseur Temporel	Veritime	Veritime	Veritime	QuickPath	QuickPath	QuickPath	PrimeTime (StaticTiming Analysis)	PrimeTime (StaticTiming Analysis)	Foundation Timing Analyzer (1:PC)
Simulateur de défaillances	Verifault	Verifault	Verifault	QuickFault	QuickFault	QuickFault	----	----	----
Vérification Formelle	----	Confrml Specman	Confrml Specman	----	FormalPro	0-in Questa	Formality Vera Leda	Formality Vera Leda	----
Génération vecteurs de test	----	Encounter Test	Encounter Test Specman Elite	FastScan FlexTest	FastScan FlexTest	Design for Test	Tetramax	Tetramax	----
Simulateur Electrique	cdsSpice Spectre Mmsim	cdsSpice Spectre Mmsim	cdsSpice Spectre Mmsim	Accusim II	Accusim II	Accusim II	Hspice	Hspice	----
Simulateur Mixte	Spectre Mmsim Verilog AMS	Spectre Mmsim Verilog AMS	Spectre Mmsim Verilog AMS	Continum	Continum	Continum	----	----	----
Placement Routage	Encounter v3.1 Silicon Ensemble 5.4.0	Encounter v4.1.5 Silicon Ensemble 5.4.12	Encounter v6.2_2 Virtuoso Layout Editor	IC blocks	IC blocks	IC blocks	Astro Jupiter XT	Astro, Astro-Rail Jupiter XT	Foundation Place & Route PAR (1:PC)
Editeur de Layout	IC Craftsman v11.1	IC Craftsman v11.1	IC Craftsman v11.2.41_5	IC Graph	IC Studio	IC Studio	----	Astro	----
Extracteur de paramètres	Diva LPE & PRE Fire & Ice	Diva LPE & PRE Fire & Ice	Diva LPE & PRE EXT	IC Extract xCalibre	IC Extract xCalibre	IC Extract xCalibre	Star RCXT	Star RCXT	Foundation Timing Extractor (1:PC)
D.R.C.	Diva drc Assura 3.0	Diva drc Assura 3.0	Assura 3.1.6	IC rules Calibre	IC rules Calibre	IC rules Calibre	Hercules	Hercules	----
E.R.C.	Diva erc Assura 3.0	Diva erc Assura 3.0	Assura 3.1.6	IC Extract Calibre, xCalibre	IC Extract Calibre, xCalibre	IC Extract Calibre, xCalibre	Hercules	Hercules	----
L.V.S.	Diva lvs Assura 3.0	Diva lvs Assura 3.0	Assura 3.1.6	IC Trace Calibre	IC Trace Calibre	IC Trace Calibre	Hercules	Hercules	----

N.B. I: Installé NI: Non installé
 F : License Flottante
 PC : Disponible sur PC (L sousentendu). Sur Station de Travail, par défaut.
 Type d'utilisation. E: Enseignement R: Recherche M: Mixte

